

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1Bw

Application of: Levine et al.

Serial No.: 10/674,642

Filed: September 30, 2003

For: Method and Apparatus to  
Autonomically Count Instruction  
Execution for Applications

35525

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

§ Group Art Unit: 2192  
§  
§ Examiner: Das, Chameli  
§  
§ Attorney Docket No.: AUS920030484US1  
§

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- Reference AC; and
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Respectfully submitted,

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**Levine et al.**

Serial No: **10/674,642**

Filed: **September 30, 2003**

Title: **Method and Apparatus to  
Autonomically Count Instruction Execution  
for Applications**

Group Art Unit: **2192**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
Austin, Texas 78758

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

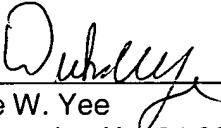
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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Respectfully submitted,


  
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Substitute for form 1449/PTO

**Complete if Known**

Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Das, Chameli
Art Unit	2192
Attorney Docket No.	AUS920030484US1



# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initial		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

<b>Examiner Initial</b>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
	AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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